

- Compiles with UL60950 for Basic Insulation
- IC Grade transfermolded packaging
- Operating Temp 0°C to 70°C (-40°C to +85°C Available)
- Half, Single and Quad port packages available
- Matched to leading transceiver ICs

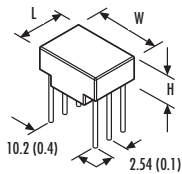
Electrical Specification @ Ta=25°C

Part #	Mounting	Turns Ratio		Inductance (mH Min.)	DCR (Ω) Pri-Sec	Leakage Inductance (μH)	Capacitance (pF MAX.)	Isolation/Hipot	Package Fig.	Schematic Fig.
		1ST	2ND							
TTC-295	T-PAK	1: 1.14CT	1CT:2CT	1.2	0.7 - 0.8	0.8	35	1500V	A	1
TTC-296	T-PAK	1:1.26	1CT:2CT	1.5	0.7 - 0.9	0.4	40	1500V	A	2
TTC-297	T-PAK	2CT:1CT	1CT:2CT	2.0	0.7 - 0.4	1.5	30	1500V	A	3
TTC-299	T-PAK	1CT:1CT	1CT:2CT	1.2	1.0 - 1.0	0.8	40	3000V	A	3
TTC-312	16PIN SOIC	2CT:1CT	1CT:2CT	1.2	0.7-1.4	0.6	30	1500V	B	4
TTC-6017	16PIN SOIC	1CT:1CT	1CT:2CT	1.2	0.7-1.2	0.6	30	1500V	C	4
TTC-6018	16PIN SOIC	1.15CT:1	1CT:2CT	1.2	0.7-1.2	0.6	30	1500V	C	5
TTC-6019	T-PAK	1CT:2CT		1.2	0.7-1.2	0.5	30	1500V	A	3
TTC-6020	16PIN SOIC	2.3CT:1	1CT:1CT	1.2	0.7	0.4	30	1500V	C	5
TTC-6021	40PIN SOIC	1CT:2	1:2CT	1.5 typ	0.8	0.5	35	1500V	D	6
TTC-6022	40PIN SOIC	1:2.4	1:1	1.0	0.6-1.0	0.5	35	1500V	D	7
TTC-6023	40PIN SOIC	1:2CT	1:2CT	1.5 typ	0.8	0.5	35	1500V	D	8
TTC-6024	16PIN SOIC	1.26:1	1.26:1	1.2	0.7	0.5	25	1500V	C	9
TTC-6025	16PIN SOIC	1:2.4	1:1	1.2	0.7	0.4	30	1500V	C	4

Mechanical Dimensions mm (inches)

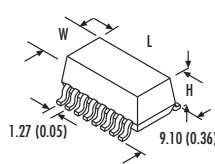
All dimensions are typical, please contact Tamura for tolerances and suggested layout information.

Fig. A



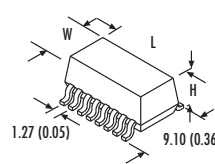
L:8.9 (0.35) W:12.7 (0.50) H:6.35 (0.25)

Fig. B



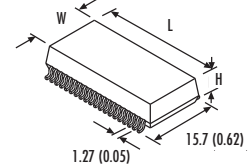
L:11.6 (0.46) W:6.91 (0.27) H:5.6 (0.22)

Fig. C



L:12.7 (0.50) W:6.91 (0.27) H:8.13 (0.32)

Fig. D



L:28.0 (1.10) W:12.2 (0.48) H:6.0 (0.24)

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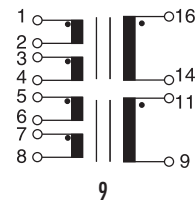
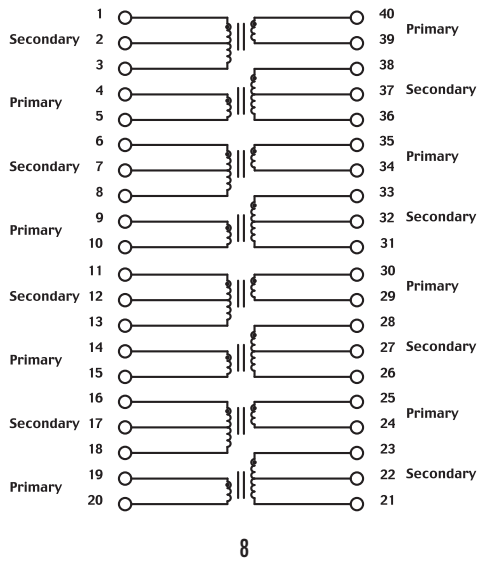
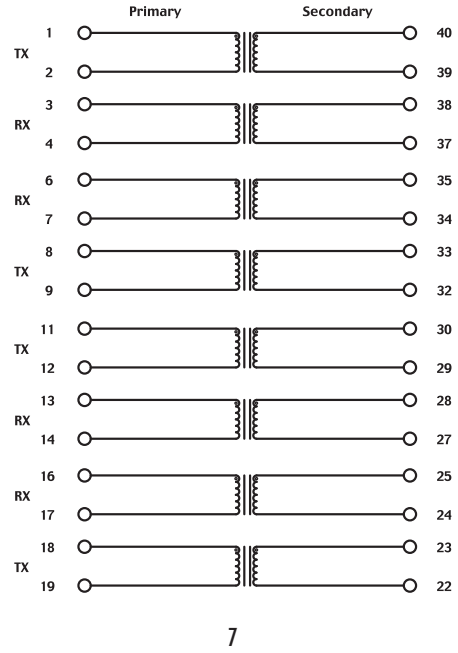
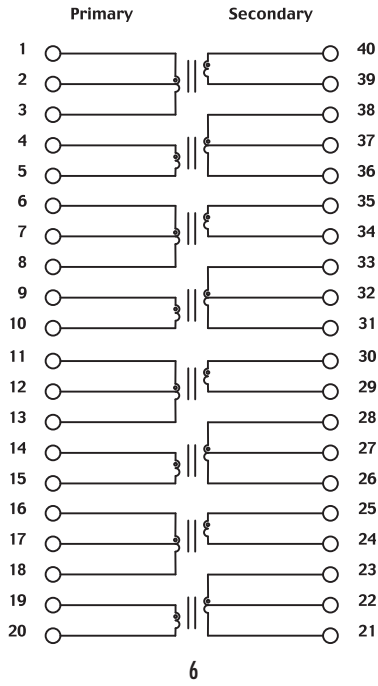
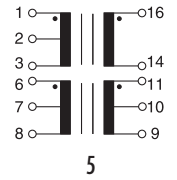
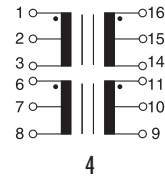
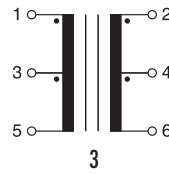
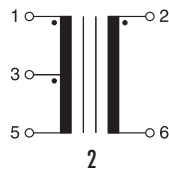
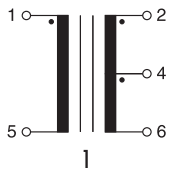
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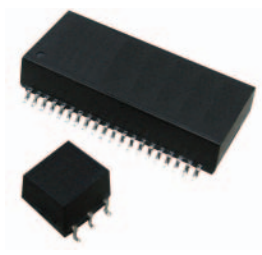
Schematics



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T3 / DS3 / E3 / STS-1



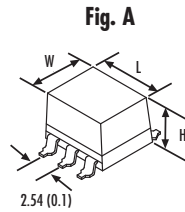
- Compiles with UL60950 for Basic Insulation
- IC Grade transfermolded packaging
- Extended Operating Temp -40°C to +85°C
- Designed for Optimal Return Loss Performance for Anex 3 and G.703
- Matched to leading transceiver ICs

Electrical Specification @ Ta=25°C

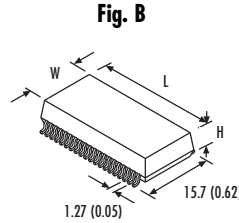
Part #	Turns Ratio		Inductance (mH Min.)	Return Loss		Insertion Loss (dB Max.)		Crosstalk (dB)		Package	Schematic
	Tx	Rx		dB Min	MHz	dB Max	MHz	dB Typ.	MHz		
TTC-339	1:2	1:1	100	-15	0.8-60	-1	0.8-60	-30	1-100	B	3
TTC-302	1:1	-	100	-15	1-100	-1	1-100	-	-	A	1
TTC-326	1:2CT	-	40	-15	1-100	-1	1-100	-	-	A	2

Mechanical Dimensions mm (inches)

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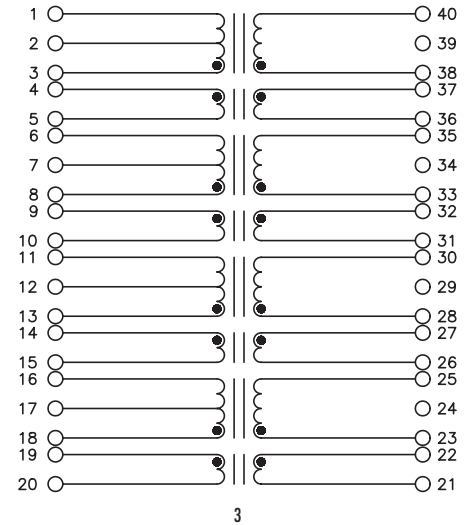
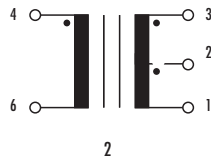
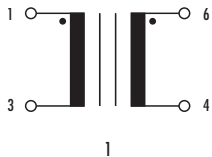


L:7.62 (0.30) W:6.86 (0.27) H:6.10 (0.24)



L:28.0 (1.10) W:12.2 (0.48) H:6.0 (0.24)

Schematics



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